



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mohan Chandra et al.

Title: DEVICE AND METHOD FOR MULTICRYSTALLINE SILICON WAFERS

Docket No.: 884.B40US1

Serial No.: 10/692611

Filed: October 24, 2003

Due Date: N/A

Examiner: Unknown

Group Art Unit: Unknown

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

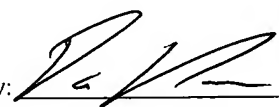
We are transmitting herewith the following attached items (as indicated with an "X"):

☒ A return postcard.

☒ An Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 15 cited documents.

If not provided in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

By: 
Atty: David C. Peterson
Reg. No. 47,857

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 9 day of February, 2004.

KACIA LEE
Name

Kacia Lee
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)

S/N 10/692611



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------|---|-----------------|------------|
| Applicant: | Mohan Chandra et al. | Examiner: | Unknown |
| Serial No.: | 10/692611 | Group Art Unit: | Unknown |
| Filed: | October 24, 2003 | Docket: | 884.B40US1 |
| Title: | DEVICE AND METHOD FOR MULTICRYSTALLINE SILICON WAFERS | | |
| Assignee: | Intel Corporation | Customer No: | 21186 |

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

The present application is either a U.S. national patent application filed after June 30, 2003 or an international application that entered the national stage under 35 U.S.C. § 371 after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

INFORMATION DISCLOSURE STATEMENT

Serial No :10/692611

Filing Date: October 24, 2003

Title: DEVICE AND METHOD FOR MULTICRYSTALLINE SILICON WAFERS

Assignee: Intel Corporation

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Dkt: 884.B40US1 (INTEL)

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

MOHAN CHANDRA ET AL.

By their Representatives,

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Date

2-9-04

By



David C. Peterson
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Name

KACIA LEE

Signature

Kacia Lee

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

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|----------------------|------------------|
| Application Number | 10/692611 |
| Filing Date | October 24, 2003 |
| First Named Inventor | Chandra, Mohan |
| Group Art Unit | Unknown |
| Examiner Name | Unknown |

Sheet 1 of 1

Attorney Docket No: 884.B40US1

US PATENT DOCUMENTS

| Examiner Initial * | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | Filing Date If Appropriate |
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| Examiner Initials* | Foreign Document No | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | T ² |
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| | JP-58-26019 | 02/16/1983 | | B28B | 007/36 | |

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials* | Cite No ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
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| | | DEIKE, RUDIGER, et al., "Reactions Between Liquid Silicon and Different Refractory Materials", J. Electrochem. Soc., Vol. 142, No. 2, (February 1995), 6 pages | |
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EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached